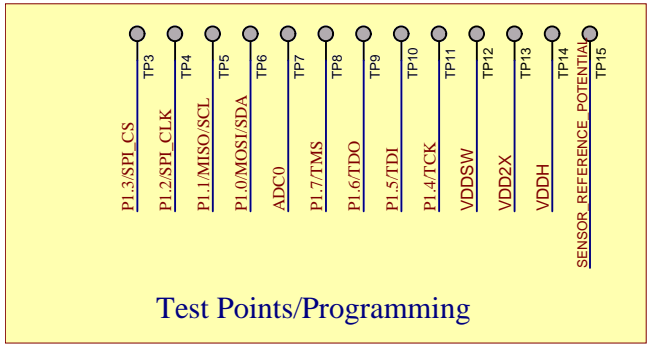
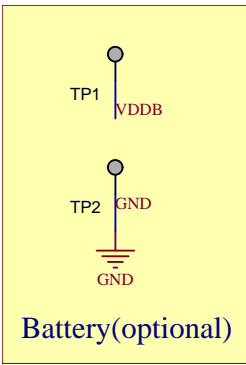
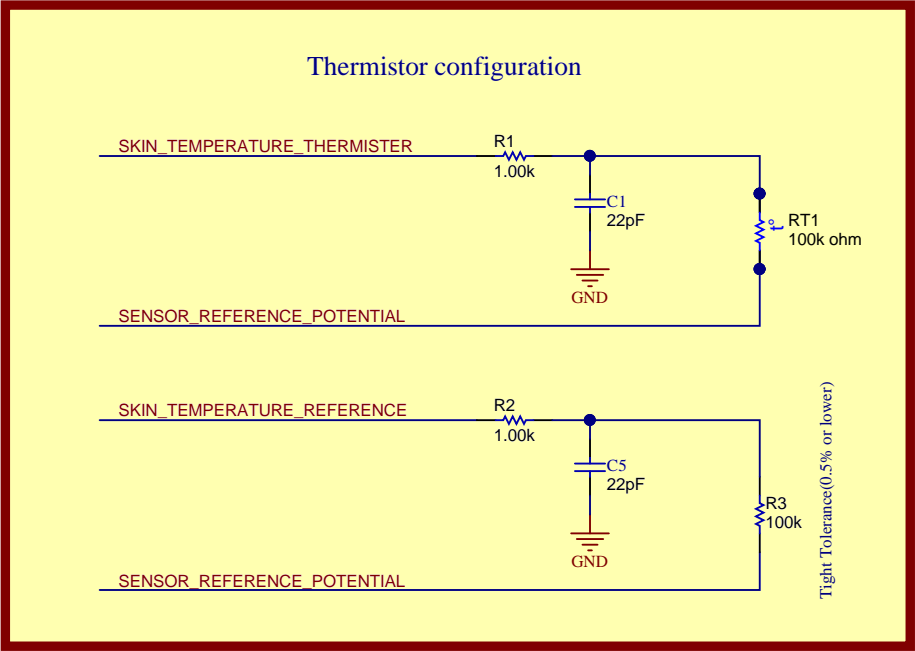
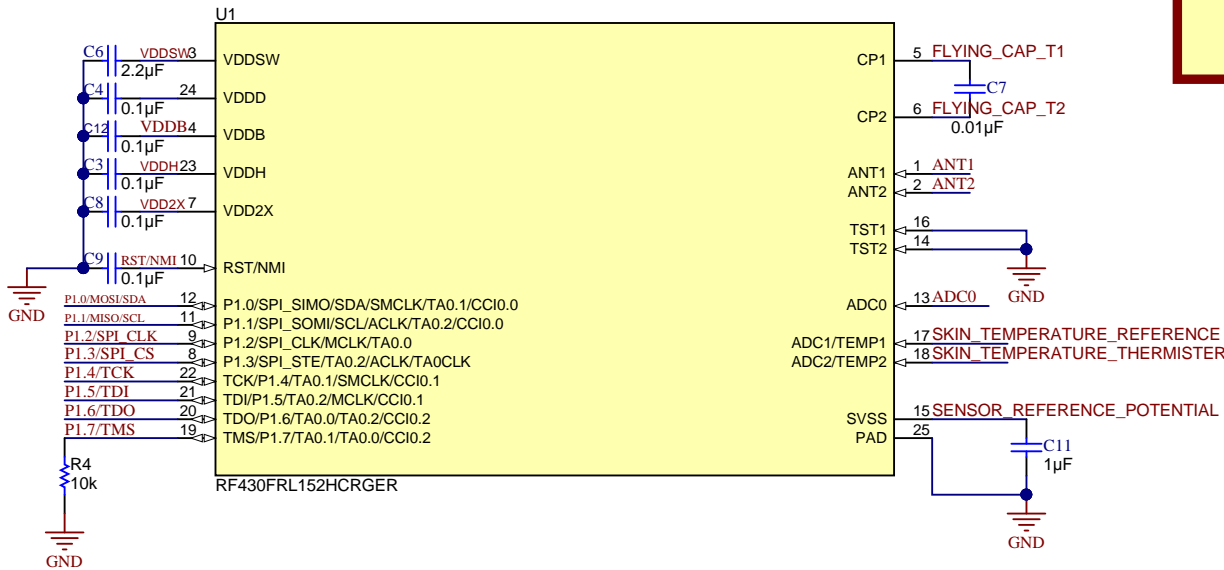
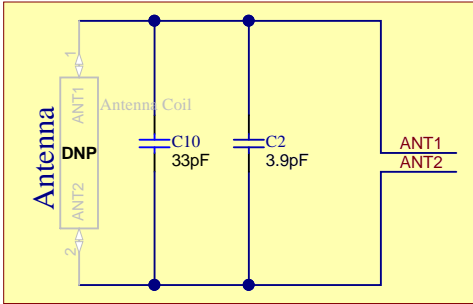


1		2		3		4		5		6							
										Revision History							
										Rev	ECN #	Approved Date	Approved by	Notes			
										N/A	N/A	N/A	N/A	N/A			
A											A						
B											B						
C											C						
D											D						
1		2		3		4		5		6							
										Orderable: RF430_TMPSNS_EVM		Designed for: Public Release		Mod. Date: 2/15/2016			
										TID #: N/A		Project Title: RF430-TMPSNS-EVM					
										Number: RF430-TMPSNS-EVM		Sheet Title:					
										SVN Rev: Version control disabled		Assembly Variant: 001				Sheet: 1 of 3	
										Drawn By:		File: RF430_TMPSNS_EVM_A_CoverSheet.SchDoc				Size: B	
										Engineer: Eddie LaCost		Contact: http://www.ti.com/support				© Texas Instruments 2016	
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1	2	3	4	5	6
A					A
B					B
C					C
D					D

PCB Number: RF430-TMPSNS-EVM
PCB Rev: A

PCB
LOGO
Texas Instruments

PCB
LOGO
Pb-Free Symbol

PCB
LOGO
FCC disclaimer

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: RF430_TMPSNS_EVM	Designed for: Public Release	Mod. Date: 2/15/2016
TID #: N/A	Project Title: RF430-TMPSNS-EVM	
Number: RF430-TMPSNS-EVM	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 3
Drawn By:	File: RF430_TMPSNS_EVM_A_Hardware.SchDoc	Size: B
Engineer: Eddie LaCost	Contact: http://www.ti.com/support	

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